

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 09991-042001	Application No. 10/642,951
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Fontaine et al.	
		Filing Date August 18, 2003	Group Art Unit 2853

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
/LN/	AA	5,975,672	11/2/1999	Wen	347	015	
/LN/	AB	6,046,822	4/4/2000	Wen et al.	358	001.900	
/LN/	AC	6,102,513	8/15/2000	Wen	347	015	
/LN/	AD	6,217,141	4/17/2001	Nakamura et al.	347	010	
/LN/	AE	6,312,076	11/6/2001	Taki et al.	347	010	
/LN/	AF	6,352,328	3/5/2002	Wen et al.	347	015	
/LN/	AG	6,517,195	2/11/2003	Koeda	347	068	
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
/LN/	AL	0 876 915	11/11/1998	EPO				
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
/LN/	AQ	David A. Johns et al., "Analog Integrated Circuit Design", John Wiley & Sons, Inc., 1997. pgs. 39-42, 396-397 and 398-400.

Examiner Signature /Lam Nguyen/	Date Considered 08/16/2007
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	